



## One (1) senior researcher & one (1) research assistant position in the project SMARTWAVE

Metamaterial based, radio frequency and millimetre wave phase shifters for future  
generation phased array antenna applications

(Call: H2020-EIC-FETPROACT-2019, GA 952088)

Funded under H2020-EU.1.2.2.-RIA



European  
Commission

Horizon 2020  
European Union funding  
for Research & Innovation

Ref. 46989

Heraklion 13/2/2021

The Institute of Electronic Structure and Laser (IESL) of the Foundation for research and Technology Hellas (FORTH), in the framework of the project SMARTWAVE, (Call: H2020-EIC-FETPROACT-2019, GA ID 952088), funded under H2020-EU.1.2.2.- Research and Innovation action, is seeking to recruit one (1) senior researcher & one (1) research assistant

### Senior Researcher

#### Job Description

The position concerns analysis of electromagnetic wave propagation in metamaterial-based phase shifters and designing of optimized tunable metamaterial-based phase shifters and other microwave circuit and antenna components. The aim is to guide the research activity and provide insight on how to separately tailor the wave propagation phase and impedance for the components, providing thus miniaturized and optimized components.

#### Required qualifications

- Bachelor in Electrical Engineering (10%)
- PhD in Physics (20%)
- Experience (at least 10 years) in the study of metamaterials (30%)
- Experience in the analysis of E/M wave interaction with complex media (30%)

#### Desirable qualifications

- Experience (>20 years) in international academic environments (5%)
- Experience in semiconductor related components (5%)

**Location:** IESL-FORTH, Heraklion Crete GREECE

**Start Date (earliest):** April 1, 2021

**Project Duration:** 9 Months with possibility of extension according to the needs of the project

**Research Assistant****Job Description**

We are looking for an integrated circuits analogue designer to work on a novel concept of high frequency integrated circuits.

**Required qualifications**

- Bachelor in Electrical and Computer engineering or similar electronics engineering degree (30%)
- Master's degree in Microelectronics or equivalent (30%)
- Experience in IC design of active circuits (Cadence or similar) (30%)

**Desirable qualifications**

- Experience in integrated amplifier design is considered an advantage (10%)

**Location:** IESL-FORTH, Heraklion Crete GREECE

**Start Date (earliest):** April 1, 2021

**Project Duration:** 12 Months with possibility of extension according to the needs of the project

**Application Submission**

Interested candidates who meet the aforementioned requirements are kindly asked to submit their applications, no later than **February 28, 2021, 23:59 local Greece** time to the address ([hr@iesl.forth.gr](mailto:hr@iesl.forth.gr)), with cc to the Dr George Deligeorgis ([deligeo@iesl.forth.gr](mailto:deligeo@iesl.forth.gr)).

**In order to be considered, the application must include:**

- Application Form (please download file from the job announcement webpage <https://www.iesl.forth.gr/en/jobs-bids/jobs/job-positions>)
- Detailed curriculum vitae (CV) of the candidate
- Scanned Copies of academic titles

**Any application received after the deadline will not be considered for the selection****Contact**

For information and questions regarding the application and selection procedure, candidates are asked to contact the secretariat ([hr@iesl.forth.gr](mailto:hr@iesl.forth.gr)), tel. +30 2810-391301.

For information and questions about the advertised position and the research activity of the group or the institute, candidates are asked to contact Dr George Deligeorgis ([deligeo@iesl.forth.gr](mailto:deligeo@iesl.forth.gr)), tel. +30 2810-394112.

**Selection Announcement**

The result of the selection will be announced on the website of IESL-FORTH.

Candidates have the right to appeal the selection decision, by addressing their written objection to the IESL secretariat within five (5) working days since the results announcement on the web. They also have the right to access (a) the files of the candidates as well as (b) the table of candidates' scores (ranking of candidates results). All the above information related to the selection procedure will be available at the secretariat of IESL-FORTH in line with the Hellenic Data Protection Authority.

**GDPR**

FORTH is compliant with all legal procedures for the processing of personal data as defined by the **Regulation EU/2016/679 on the protection of natural persons with regard to the processing of personal data**. FORTH processes the personal data and relevant supporting documents that you have submitted to us. Processing of that data is carried out exclusively for the needs and purposes of this specific call. Such data shall not be transmitted to or communicated to any third party unless required by law. FORTH retains the above data up to the announcement of the final results of the call, unless further process and reservation is required by law or for purposes of exercise, enforcement, prosecution of certain one's legitimate legal rights' as defined in the Regulation EU/2016/679 and/or in national law. We inform you that under the Regulation EU/2016/679 you have the rights to be informed about your personal data, access to, rectification and erasure, restrictions of process and objection to as provided by applicable regulation and national laws. We acknowledge also to you, that you have the right to file a complaint to the national Data Protection Authority. For any further information regarding exercise of your personal data protection rights, you may contact the Data Protection Officer at FORTH at [dpo@admin.forth.gr](mailto:dpo@admin.forth.gr). You have the right to withdraw your application and consent for the processing of your personal data at any time. We inform you that, in this case, FORTH shall destroy such documents and/or supporting documents submitted and shall delete the related personal data.