

## CURRICULUM VITAE

### PERSONAL DATA

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### EDUCATION

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<b>2012 - present</b>	PhD student at Physics Department, University of Crete, Greece
<b>2004 - 2009</b>	BS, MS, Electrical and Computer Engineering, Aristotle University of Thessaloniki, Greece

### WORK EXPERIENCE

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<b>10/2015 - present</b>	Research work on projects provided by IESL/FORTH
<b>10/2012-09/2015</b>	Research work on project with title : "Spontaneous growth, properties and devices of III-V semiconductor nanowires", funded by Greek National Program "Thales"
<b>07/2011-08/2012</b>	Collaboration with technical engineering office in the area of designing electromechanical, photovoltaic and solar thermal installations
<b>12/2011-06/2012</b>	Fixed-term contract at the Technical Service of the Municipality of Malevizi, Heraklion, Crete, Greece
<b>02/2010-10/2010</b>	Secretarial Support - Military Service

## SKILLS

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<b>Photomask design</b>	Photomask design for the fabrication of devices using Clewin WieWeb or Autocad CAD software
<b>Device fabrication</b>	Semiconductor device fabrication focused on GaN FinFETs and vertical GaN NW transistors using a variety of nanofabrication techniques (i.e. lithography, e-beam evaporation, RIE, ALD, CVD)
<b>Device characterization</b>	Semiconductor device DC characterization using Keithley 4200-SCS semiconductor characterization system
<b>Device simulation</b>	Semiconductor device simulation focused on GaN FinFETs and vertical GaN NW transistors using Silvaco Atlas software